

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:

Raul-Adrian Cernea et al.

Title:

Non-Volatile Memory and Method With Bit Line to Bit Line Coupled

Compensation

Application No.:

10/667,222

Filing Date:

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Examiner:

Hoang, Huan

Group Art Unit:

2818

Docket No.:

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I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on August 9, 2005

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Commissioner for Patents

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Dear Sir:

This is a Request for Continued Examination (RCE) under 37 C.F.R. § 1.114 of the Please consider the Supplemental Information Disclosure above-identified application. Statement, which is being filed herewith. The RCE fee required under 37 C.F.R. § 1.17(e) is authorized in an accompanying transmittal letter. Please contact the undersigned attorney with any questions concerning this request or the above-identified patent application.

REQUEST FOR CONTINUED EXAMINATION (RCE)

Respectfully submitted,

Gerald P. Parsons

Date

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